| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2022/06/17 |
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| **System BIOS:** CL600F09  **EC BIOS:** 006 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.10.1288  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Jack Bai | | | **Approved By:** Vito Tsai |
| 1 | fix When system force shutdown with HD audio disabled in BIOS setup menu issue | | |
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| **EC Firmware Release Note** | | | |
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| |  |  |  |  | | --- | --- | --- | --- | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | | CL600F09.bin | F09 | 9D84 | 924D | | For Factory | F09 | N/A | 5366 |   Note: | | | |

| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2022/04/29 |
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| **System BIOS:** CL600F08  **EC BIOS:** 006 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.10.1288  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Jack Bai | | | **Approved By:** Vito Tsai |
| 1 | modify boot option names into next line for boot option name out of boundary | | |
| 2 | add warning message for creating new ssd password | | |
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| **EC Firmware Release Note** | | | |
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| |  |  |  |  | | --- | --- | --- | --- | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | | CL600F08.bin | F08 | 8712 | 5542 | | For Factory | F08 | N/A | 2CDD |   Note: | | | |

| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2021/11/05 |
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| **System BIOS:** CL600F07  **EC BIOS:** 006 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.10.1288  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Jack Bai | | | **Approved By:** Vito Tsai |
| 1 | update EC to EC REV 006(CL66) | | |
| 2 | update verbtable HDACfg-0256-18540344-1.35W-4ohm-Box-mono\_SPK\_removeDMIC\_\_modifymonoSPKtostereo-20210831.txt | | |
| 3 | fix third-party utility can't adjust boot device priority by bootorder issue | | |
| 4 | add feature function for saving and restoring RTC time after power reset | | |
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| **EC Firmware Release Note** | | | |
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| |  |  |  |  | | --- | --- | --- | --- | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | | CL600F07.bin | F07 | DABA | 3CD0 | | For Factory | F07 | N/A | D7FA |   Note: | | | |

| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2020/06/07 |
| --- | --- | --- | --- |
| **System BIOS:** CL600F06  **EC BIOS:** 005 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.10.1288  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Jack Bai | | | **Approved By:** Vito Tsai |
| 1 | Add ALC256 verb table | | |
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| **EC Firmware Release Note** | | | |
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| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2020/12/01 |
| --- | --- | --- | --- |
| **System BIOS:** CL600F05  **EC BIOS:** 005 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.10.1288  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Jack Bai | | | **Approved By:** Vito Tsai |
| **Item.** | fix BIOS infinite booting  if hold press power buttun more than 20 seconds under the setup menu issue | | |
| 1 | fix QTC bug infinite booting after removing SSD 5 times issue | | |
| 2 | add function for BIOS recovery retry，BIOS would try to boot formaly after BIOS recovery binary retry fail  3 times | | |
| 3 | Fix USB lock issue | | |
| 4 | fix install ubuntu fail by customer’s method issue | | |
| 5 | fix BIOS infinite booting  if hold press power buttun more than 20 seconds under the setup menu issue | | |
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| **EC Firmware Release Note** | | | |
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| |  |  |  |  | | --- | --- | --- | --- | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | | CL600F05.bin | F05 | E56B | 8492 | | For Factory | F05 | N/A | ABAC |   Note: | | | |

| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2020/01/09 |
| --- | --- | --- | --- |
| **System BIOS:** CL600F03  **EC BIOS:** 005 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.10.1288  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Archie Zhou | | | **Approved By:** Lu\_Zhang |
| **Item.** | Root cause and change description | | |
| 1 | Per LG requirement, modify different verb tables for linux/windows | | |
| 2 | Per LG requirement, implement open source license feature in the Setup Menu | | |
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| **EC Firmware Release Note** | | | |
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| |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | Checksum: | | | | | | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | BIOS Checksum | | CL600F03.bin | F03 | 0D52 | 67E2 | 4CE1 | |  |  |  |  |  | |   Note: | | | |

| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2019/08/26 |
| --- | --- | --- | --- |
| **System BIOS:** CL600F02  **EC BIOS:** 005 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.10.1288  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Archie Zhou | | | **Approved By:** Lu\_Zhang |
| **Item.** | Root cause and change description | | |
| 1 | Customer need to support power last state | | |
| 2 | Modify EMMC speed as post time HS200 and windows with HS400 | | |
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| **EC Firmware Release Note** | | | |
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| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2019/06/11 |
| --- | --- | --- | --- |
| **System BIOS:** CL600F01  **EC BIOS:** 005 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.10.1288  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Lu\_Zhang | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Fixed system will hang on in password input when system reboot with password set up. | | |
| 2 | Fixed 4k resolution panel can’t set up flash rate to 60Hz. | | |
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| **EC Firmware Release Note** | | | |
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| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2019/05/29 |
| --- | --- | --- | --- |
| **System BIOS:** CL600F00  **EC BIOS:** 005 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.10.1288  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Lu\_Zhang | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Modify BIOS version to F00 for MP | | |
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| **EC Firmware Release Note** | | | |
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| |  |  |  |  | | --- | --- | --- | --- | | Checksum: | | | | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | | CL600F00.bin | F00 | 237D | E2B1 | |  |  |  |  |   Note: | | | |

| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2019/04/30 |
| --- | --- | --- | --- |
| **System BIOS:** CL600B16  **EC BIOS:** 004 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.10.1288  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Lu\_Zhang | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Disable Fast Boot in setup menu | | |
| 2 | 1.Remove "Wake on LAN Form S5"  2.Enable "Wake on LAN" | | |
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| **EC Firmware Release Note** | | | |
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| |  |  |  |  | | --- | --- | --- | --- | | Checksum: | | | | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | | CL600B16.bin | B16 | C505 | D82F | |  |  |  |  |   Note: | | | |

| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2019/04/19 |
| --- | --- | --- | --- |
| **System BIOS:** CL600B15  **EC BIOS:** 004 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.10.1288  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Lu\_Zhang | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | 1.Set PSV and AUX1 to 90  2.Set PL1.min to 5W  3.Set sample period to 2 sec  4.Set Hot to 105 | | |
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| **EC Firmware Release Note** | | | |
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| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2019/03/22 |
| --- | --- | --- | --- |
| **System BIOS:** CL600B14  **EC BIOS:** 004 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.10.1288  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Lu\_Zhang | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | 1.Modify "Wake on LAN Form S5" to "Wake on LAN From S5"  2.Enable "Wake on LAN From S5" | | |
| 2 | Modify "Wake on LAN From S5" and "DVMT Pre-Allocated change condition | | |
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| **EC Firmware Release Note** | | | |
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| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2019/03/21 |
| --- | --- | --- | --- |
| **System BIOS:** CL600B13  **EC BIOS:** 004 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.10.1288  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Lu\_Zhang | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Use TCO reset OS when system hang 0xA6 | | |
| 2 | 1.Change check setup menu password condition  2.Modify error message to "Incorrect password" | | |
| 3 | 1.Remove RTC wake up form S4 function  2.Add Lan wake up form S5  3.Add "Wake on LAN Form S5" item in setup menu for control Lan wake up form S5 | | |
| 4 | Entry setup menu hidden page by ctrl+alt+f3 | | |
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| **EC Firmware Release Note** | | | |
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| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2019/03/15 |
| --- | --- | --- | --- |
| **System BIOS:** CL600B12  **EC BIOS:** 004 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.10.1288  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Lu\_Zhang | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Update TXE to 4.0.10.1288 | | |
| 2 | Add DVMT Pre-Allocated item to Advanced\MISC configuration for user adjust Video RAM size in setup menu | | |
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| **EC Firmware Release Note** | | | |
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| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2019/03/01 |
| --- | --- | --- | --- |
| **System BIOS:** CL600B11  **EC BIOS:** 004 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.5.1280  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Lu\_Zhang | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Project do not support S4 function ,but the "RTC Wake Up from S4"option shown in Advanced\MISC Configuration in setup menu | | |
| 2 | Can not show EMMC info in setup menu Main page | | |
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| **EC Firmware Release Note** | | | |
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| |  |  |  |  | | --- | --- | --- | --- | | Checksum: | | | | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | | CL600B11.bin | B11 | 002E | E10D | |  |  |  |  |   Note: | | | |

| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2019/02/15 |
| --- | --- | --- | --- |
| **System BIOS:** CL600B10  **EC BIOS:** 004 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.5.1280  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Lu\_Zhang | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Update HOT to 105 | | |
| 2 | Remove S4 | | |
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| **EC Firmware Release Note** | | | |
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| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2019/01/15 |
| --- | --- | --- | --- |
| **System BIOS:** CL600B09  **EC BIOS:** 004 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.5.1280  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Wei6\_Xia | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Update EC binary to REV 004 | | |
| 2 | Patch DPTF, reference CL66\_DPTF&EC\_Table\_1206.xlsx | | |
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| **EC Firmware Release Note** | | | |
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| |  |  |  |  | | --- | --- | --- | --- | | Checksum: | | | | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | | CL600B09.bin | B09 | 0A04 | B3E9 | |  |  |  |  |   Note: | | | |

| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2019/01/11 |
| --- | --- | --- | --- |
| **System BIOS:** CL600B08  **EC BIOS:** 003 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.5.1280  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Wei6\_Xia | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Update EC binary to REV 003 | | |
| 2 | Follow driver request, refer HDACfg-0233-VB-18540344-CL66-BOX- mono out.txt | | |
| 3 | Modify third boot device "SATA HDD" to "SATA SSD" | | |
| 4 | Modify fourth boot device "SATA SSD" to "SATA HDD" | | |
| 5 | Modify "Supervisior Password" to "Supervisor Password" | | |
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| **EC Firmware Release Note** | | | |
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| |  |  |  |  | | --- | --- | --- | --- | | Checksum: | | | | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | | CL600B08.bin | B08 | 1C2A | AF22 | |  |  |  |  |   Note: | | | |

| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2018/12/21 |
| --- | --- | --- | --- |
| **System BIOS:** CL600B07  **EC BIOS:** 002 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.5.1280  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Wei6\_Xia | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Fix issue, One of usb 2.0 port won't display safety remove icon after plug in usb Flash or HDD. | | |
| 2 | Fix issue, After plug in usb Flash or HDD, device won't show in Devices and Printers. | | |
| 3 | Set pcie\_wake3\_enable value is 1 and pcie 3 wake disable value is 0 | | |
| 4 | follow customer request, change SSID  AIO SSID: 0345  BOX SSID: 0344 | | |
| 5 | follow customer request, change audio verbtable  AIO: HDACfg-0235-18540345-CL66-AIO.TXT  BOX: HDACfg-0233-VB-18540344-CL66-BOX.TXT | | |
| 6 | Report XHC device to wake up from only S3. | | |
| 7 | Update PXE Rom to 2.043 | | |
| 8 | Modify "Suppervisior Password" to "Supervisior Password" | | |
| 9 | Modify "Enable (Setup mode)" to "Disabled (Setup mode)" | | |
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| **EC Firmware Release Note** | | | |
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| |  |  |  |  | | --- | --- | --- | --- | | Checksum: | | | | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | | CL600B07.bin | B07 | 1A5C | B149 | |  |  |  |  |   Note: | | | |

| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2018/12/14 |
| --- | --- | --- | --- |
| **System BIOS:** CL66\_V006  **EC BIOS:** 002 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.5.1280  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Wei6\_Xia | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Follow customer request, add customer string. | | |
| 2 | Fix issue, Even though Secure key was deleted, Protected signature is displayed as enabled on Setup-Security Menu. | | |
| 3 | Complete camera definition. | | |
| 4 | Fix issue, Camera exist in Devices and Printers. | | |
| 5 | Fix issue, CL66 doesn't distinguish between uppercase and lowercase letters. | | |
| 6 | Follow customer request, Change the description of SSD password input request window  \* Current : Enter User Password  \* To be : Enter SSD Password | | |
| 7 | Update EC binary to REV 002. | | |
| 8 | Set EFP3 swing-2 and Type-C DP port swing-3 | | |
| 9 | Set SMBUS clock less than 100Khz | | |
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| **EC Firmware Release Note** | | | |
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| |  |  |  |  | | --- | --- | --- | --- | | Checksum: | | | | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | | CL66\_ BIOS\_V006.bin | 006 | 164E | 378A | |  |  |  |  |   Note: | | | |

| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ032 | **Date:** 2018/12/07 |
| --- | --- | --- | --- |
| **System BIOS:** CL66\_V005  **EC BIOS:** 001 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.5.1280  **Microcode:** M01706A1\_00000028.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.8.0 |
| **Released By:** Wei6\_Xia | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Set DEFAULT\_USB\_SUPPORT\_SETTING is Partial Initial | | |
| 2 | Enable fast boot | | |
| 3 | Add SMI to trigger SMI\_WAK to clean CMOS 0x79 cleep type to 00 | | |
| 4 | Update EC binary to REV 001 | | |
| 5 | Update TXE to 4.0.5.1280 | | |
| 6 | Remove configuration "Enable eDP output, set LFP and eDP always output at during post" | | |
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| **EC Firmware Release Note** | | | |
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| |  |  |  |  | | --- | --- | --- | --- | | Checksum: | | | | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | | CL66\_ BIOS\_V005.bin | 005 | 2523 | 9D8B | |  |  |  |  |   Note: | | | |

| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ020 | **Date:** 2018/11/30 |
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| **System BIOS:** CL66\_V004  **EC BIOS:** 000 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.0.1249  **Microcode:** M01706A1\_00000022.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.0 |
| **Released By:** Wei6\_Xia | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Enable eDP output, set LFP and eDP always output at during post | | |
| 2 | Disable fast boot | | |
| 3 | Disable native aspm, remove OS control | | |
| 4 | Per EA request to modify USB 2.0 Port 1-7 driving setting, Set USBPP1[13-8] to 100000. | | |
| 5 | Update PXE Rom | | |
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| **EC Firmware Release Note** | | | |
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| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ020 | **Date:** 2018/11/23 |
| --- | --- | --- | --- |
| **System BIOS:** CL66\_V003  **EC BIOS:** 000 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.0.1249  **Microcode:** M01706A1\_00000022.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.0 |
| **Released By:** Wei6\_Xia | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Set PL1=10W and PL2=25W | | |
| 2 | Remove sleep button asl code | | |
| 3 | Remove battery and Lid related code in SX and SW callback | | |
| 4 | Read CPU temperature and write back to EC RAM 0x3E2 | | |
| 5 | Remove PS/2 keyboard and mouse | | |
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| **EC Firmware Release Note** | | | |
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| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ020 | **Date:** 2018/11/16 |
| --- | --- | --- | --- |
| **System BIOS:** CL66\_V002  **EC BIOS:** 000 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.0.1249  **Microcode:** M01706A1\_00000022.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.0 |
| **Released By:** Wei6\_Xia | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | change GOP setting | | |
| 2 | Remove lid in ASL | | |
| 3 | Remove LG Airplane mode button and platform manager | | |
| 4 | Remove battery in ASL | | |
| 5 | Close D3 cold support | | |
| 6 | Set DEFAULT\_USB\_SUPPORT\_SETTING is Full initial | | |
| 7 | change CL66 Intel VGA SSID to 0x0339/0x0340 | | |
| 8 | Set PCIE port 5 Selectable De-emphasis to disable | | |
| 9 | Set PCIE port 5 ASPM to auto | | |
| 10 | Fix issue, DTPM can’t detect by OS | | |
| **EC Firmware Release Note** | | | |
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| |  |  |  |  | | --- | --- | --- | --- | | Checksum: | | | | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | | CL66\_ BIOS\_V002.bin | 002 | 8ABC | 3041 | |  |  |  |  |   Note: | | | |

| **Product Model:** CL66 | | **BIOS Kernel:** 5.13\_VEB\_1AUAJ020 | **Date:** 2018/11/12 |
| --- | --- | --- | --- |
| **System BIOS:** CL66\_V001  **EC BIOS:** 000 | | **Intel GOP Driver:** 13.0.1018  **TXE FW Version:** 4.0.0.1249  **Microcode:** M01706A1\_00000022.PDB  **PMC FW Version:** 6.05 | **Reference code:** v2.0.0 |
| **Released By:** Wei6\_Xia | | | **Approved By:** Thomas\_Lin |
| **Item.** | Root cause and change description | | |
| 1 | Remove AC and Battery check when update bios | | |
| 2 | Patch SSID/SVID and Audio Verbtable | | |
| 3 | Patch PCIE CLK REQ | | |
| 4 | Modify PCIE port 5 ASPM to Disable | | |
| 5 | Modify GPIO\_44 and GPIO\_45 to GPI | | |
| 6 | swap GPIO\_140/GPIO\_141 setting in BIOS code | | |
| 7 | Modify PCIe/USB3 Combo Port 0,1,2 to PCIe | | |
| 8 | Set GPIO\_120 and GPIO\_122 and GPIO\_123 to external pull high | | |
| **EC Firmware Release Note** | | | |
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| |  |  |  |  | | --- | --- | --- | --- | | Checksum: | | | | | File | Version | (ALL) Checksum | (Skip DMI & ESCD) Checksum | | CL66\_ BIOS\_V001.bin | 001 | F317 | 9DCA | |  |  |  |  |   Note: | | | |